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## **Storage Efficient Hardware Prefetching using Delta Correlating Prediction Tables**

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# Delta Correlating Prefetch Tables

- Perez et al. did a comparative survey of hardware prefetchers in 2004.
  - Reference Prediction Tables and PC/DC using a Global History Buffer
- Delta Correlating Prediction Tables **combines** these two approaches and adds extra control for avoiding duplicate prefetching.
- Perez et al. also found that you can make **anything** look good provided the right benchmarks and parameters.

# Prefetcher Overview

Sequential

RPT

PC/DC

DCPT





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# Prefetcher Overview

	Sequential	RPT	PC/DC	DCPT
Sequential				

# Prefetcher Overview

	Sequential	RPT	PC/DC	DCPT
Sequential	✓	✓	✓	✓
Constant Stride	✗	✓	✓	✓

# Prefetcher Overview

	Sequential	RPT	PC/DC	DCPT
Sequential	✓	✓	✓	✓
Constant Stride	✗	✓	✓	✓
Repeating Pattern	✗	✗	✓	✓

# Prefetcher Overview

	Sequential	RPT	PC/DC	DCPT
Sequential	✓	✓	✓	✓
Constant Stride	✗	✓	✓	✓
Repeating Pattern	✗	✗	✓	✓
Complexity	⊕ ⊕	⊕	⊖ ⊖	⊖

# Prefetcher Overview

	Sequential	RPT	PC/DC	DCPT
Sequential	✓	✓	✓	✓
Constant Stride	✗	✓	✓	✓
Repeating Pattern	✗	✗	✓	✓
Complexity	⊕ ⊕	⊕	⊖ ⊖	⊖
Delay	⊕ ⊕	⊕ ⊕	⊖ ⊖	⊕



# Prefetcher Overview

	Sequential	RPT	PC/DC	DCPT
Sequential	✓	✓	✓	✓
Constant Stride	✗	✓	✓	✓
Repeating Pattern	✗	✗	✓	✓
Complexity	⊕ ⊕	⊕	⊖ ⊖	⊖
Delay	⊕ ⊕	⊕ ⊕	⊖ ⊖	⊕
Storage Efficiency		⊕	⊖	⊕

# Outline

Motivation

Reference Prediction Tables

Properties of RPT prefetching

PC/DC Prefetching

Global History Buffer

Delta Correlation

Properties of PC/DC prefetching

Delta Correlating Prefetch Tables

DCPT Properties

Results

Concluding Remarks

# Reference Prediction Tables

Cache Miss:

PC	Last Addr.	Delta	State



# Reference Prediction Tables

Cache Miss: 1

PC	Last Addr.	Delta	State



# Reference Prediction Tables

Cache Miss: 1

100	1	--	Init
PC	Last Addr.	Delta	State



# Reference Prediction Tables

Cache Miss: 1 3

100	3 <sub>1</sub>	2 <sub>--</sub>	Train
PC	Last Addr.	Delta	State



# Reference Prediction Tables

Cache Miss: 1 3 5

100	5 <sub>3</sub>	2 <sub>2</sub>	Prefetch
PC	Last Addr.	Delta	State



# Properties of RPT prefetching

- Very high accuracy
- Relatively low cost - Table lookup, comparator and subtraction
- Small memory footprint
- Only able to capture constant strides



# Global History Buffer

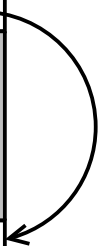
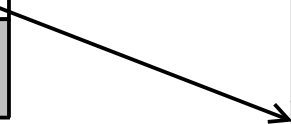
Index Table

PC	Ptr
100	●

Delta Buffer


Global History Buffer

Address	Ptr
3	●
1	



# Global History Buffer

Index Table

PC	Ptr
100	●

Delta Buffer


Global History Buffer

Address	Ptr
5	
3	●
1	

# Global History Buffer

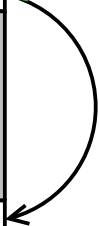
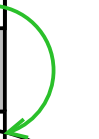
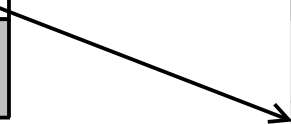
Index Table

PC	Ptr
100	●

Delta Buffer


Global History Buffer

Address	Ptr
5	●
3	●
1	



# Global History Buffer

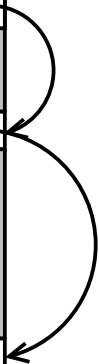
Index Table

PC	Ptr
100	●

Delta Buffer


Global History Buffer

Address	Ptr
5	●
3	●
1	



# Global History Buffer

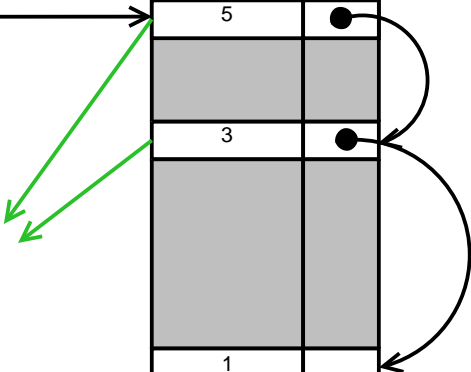
Index Table

PC	Ptr
100	●

Delta Buffer


Global History Buffer

Address	Ptr
5	●
3	●
1	



# Global History Buffer

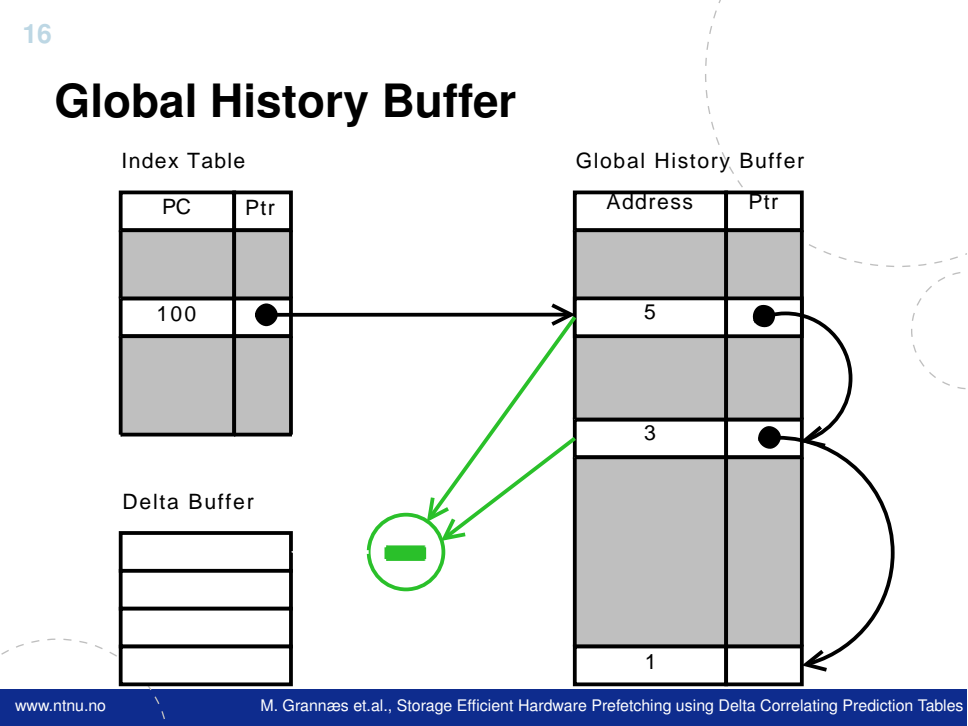
Index Table

PC	Ptr
100	●

Delta Buffer


Global History Buffer

Address	Ptr
5	●
3	●
1	



# Global History Buffer

Index Table

PC	Ptr
100	●

Global History Buffer

Address	Ptr
5	●
3	●
1	

Delta Buffer

2



# Global History Buffer

Index Table

PC	Ptr
100	●

Global History Buffer

Address	Ptr
5	●
3	●
1	

Delta Buffer

2
2





# Delta Correlation

10

11

13

16

17

19

22

# Delta Correlation

10	11	13	16	17	19	22
1	2	3	1	2	3	

# Delta Correlation

1

2

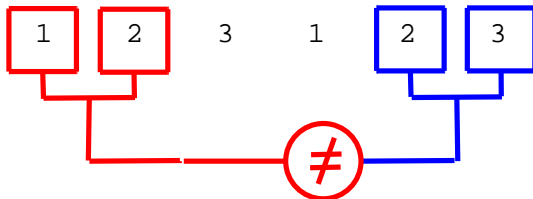
3

1

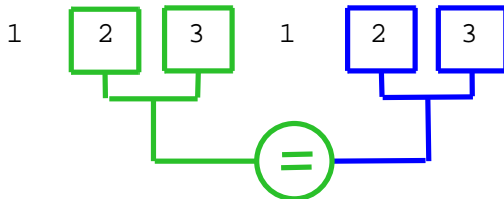
2

3

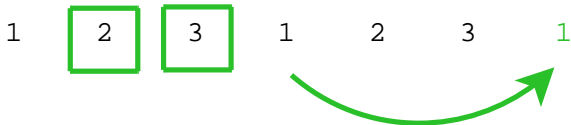
# Delta Correlation



# Delta Correlation



# Delta Correlation



# Delta Correlation

10	11	13	16	17	19	22
1	2	3	1	2	3	1



# Delta Correlation

10	11	13	16	17	19	22	23
1	2	3	1	2	3	1	



# Delta Correlation

10	11	13	16	17	19	22	23	25
	1	2	3	1	2	3	1	2

# Properties of PC/DC prefetching

- Can capture a very wide range of patterns
- High accuracy and performance
- The global history must be very large to capture relevant data
- Pointer chasing
- The deltas are recalculated every time
- The number of deltas can vary

# Delta Correlating Prefetch Tables

PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr

# Delta Correlating Prefetch Tables

10

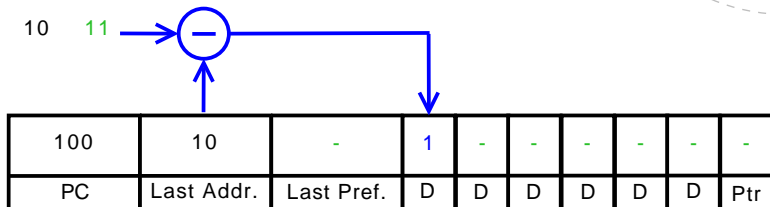
100	10	-	-	-	-	-	-	-	-
PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr

# Delta Correlating Prefetch Tables

10 11

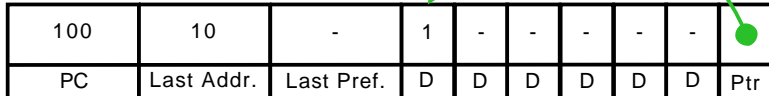
100	10	-	-	-	-	-	-	-	-
PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr

# Delta Correlating Prefetch Tables



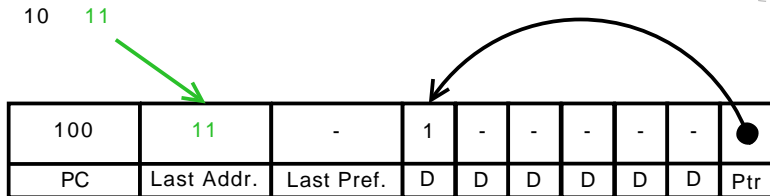
# Delta Correlating Prefetch Tables

10 11



100	10	-	1	-	-	-	-	-	●
PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr

# Delta Correlating Prefetch Tables





# Delta Correlating Prefetch Tables

10    11    13

100	13	-	1	2	-	-	-	-	●
PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr


# Delta Correlating Prefetch Tables

10   11   13   16

100	16	-	1	2	3	-	-	-	●
PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr

# Delta Correlating Prefetch Tables

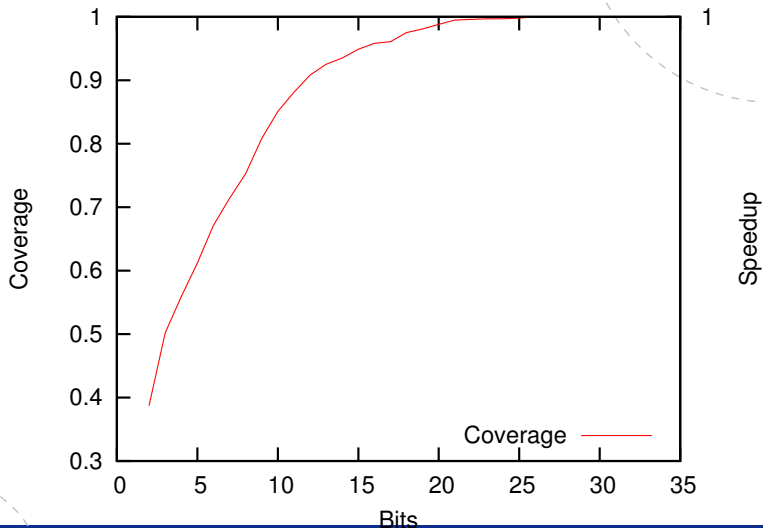
10 11 13 16 17 19 22

100	22	-	1	2	3	1	2	3	
PC	Last Addr.	Last Pref.	D	D	D	D	D	D	Ptr

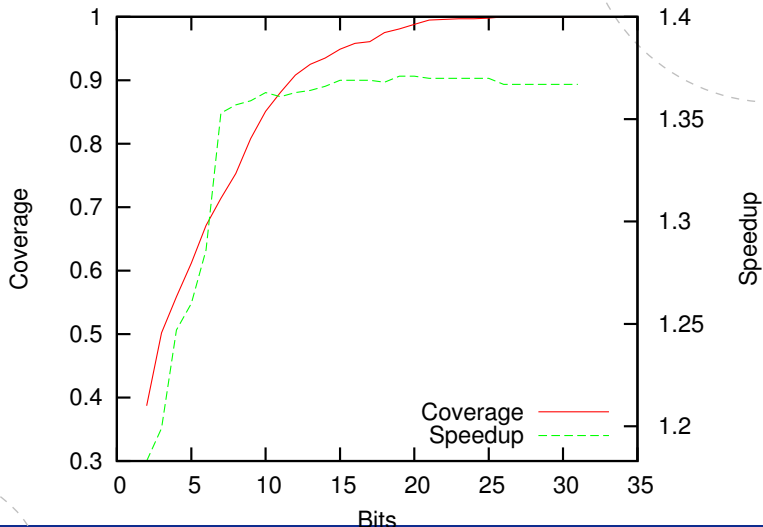
# DCPT Properties

- Able to capture the same patterns as PC/DC
- Only stores deltas
  - Uses less memory to store the same data
  - No need to recalculate the deltas
  - Fixed number of deltas - Fixed timeliness
- Constant delay
- Tracks issued prefetches to avoid overlap

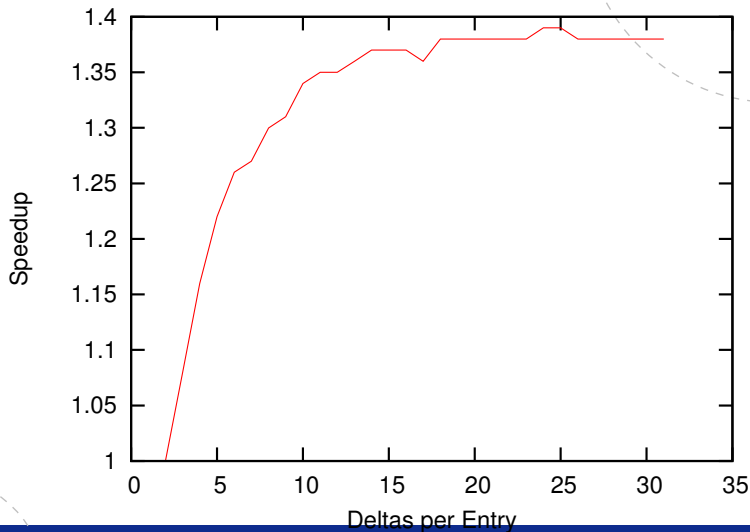
# Number of bits used to represent a delta



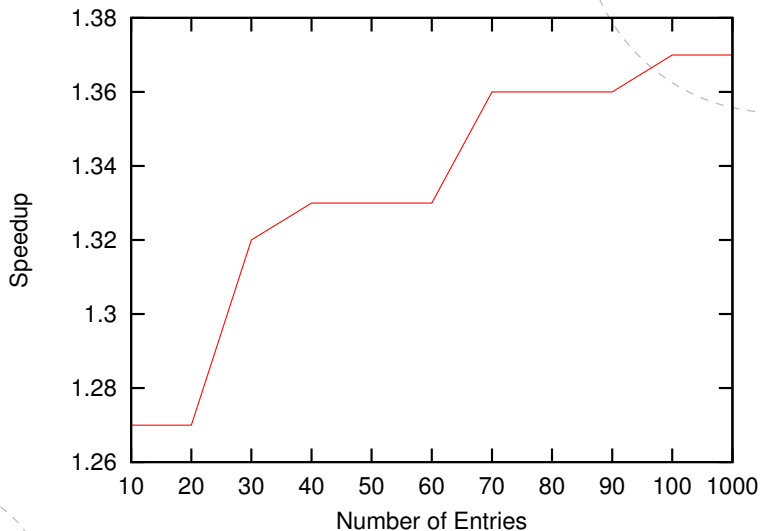
# Number of bits used to represent a delta



# Deltas per table entry

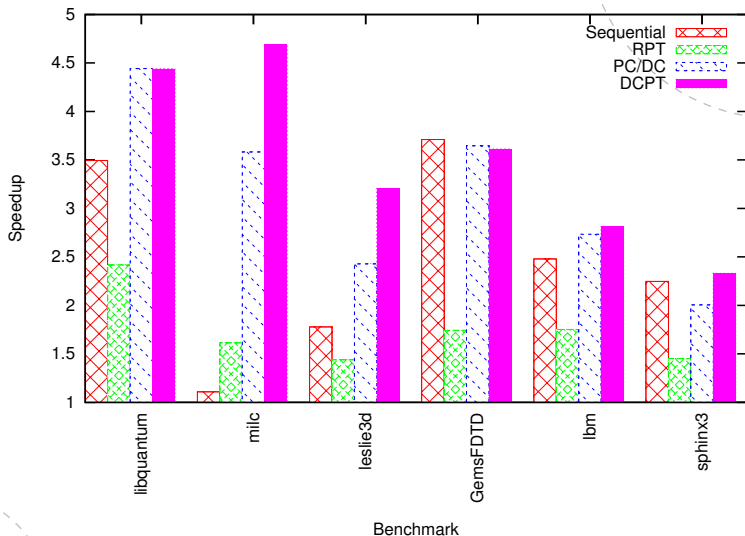


# Number of table entries

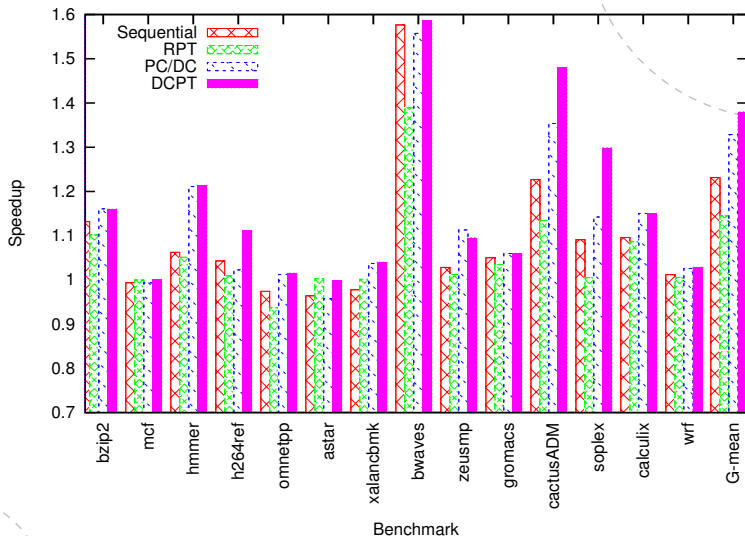




# Results I



# Results II



# Concluding Remarks

- Delta Correlating Prediction Tables is a hybrid of RPT and PC/DC prefetching.
- Combines the table based design of RPT and the pattern matching techniques of PC/DC.
- Compact representation
- Calculation in constant time

# Thank you for listening

Are there any questions?